Circuits and Logic

Assignment Rubric

Check Items

Description	Points	Comments	Grade
A 4-bit ripple carry adder is demonstrated using Logisim-evolution.	40		
The truth table for a 4-bit ripple carry adder is demonstrated using Logisim-evolution.	10		
A Karnaugh Map for a 4-bit ripple carry adder is demonstrated using Logisim-evolution.	10		
A demonstration is presented in which Logisim-evolution is used to simulate adding two 4-bit binary numbers together by means of a correctly implemented ripple carry adder.	40		

Don't Do These Things

Description	Points	Comments	Grade
The demonstration is given using screenshots instead of video.	-100		
The submitted video lacks audio narration.	-100		

Grade

Calculation Algorithm	Your Grade
max(sum of above grades, 0)	x

Remarks

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